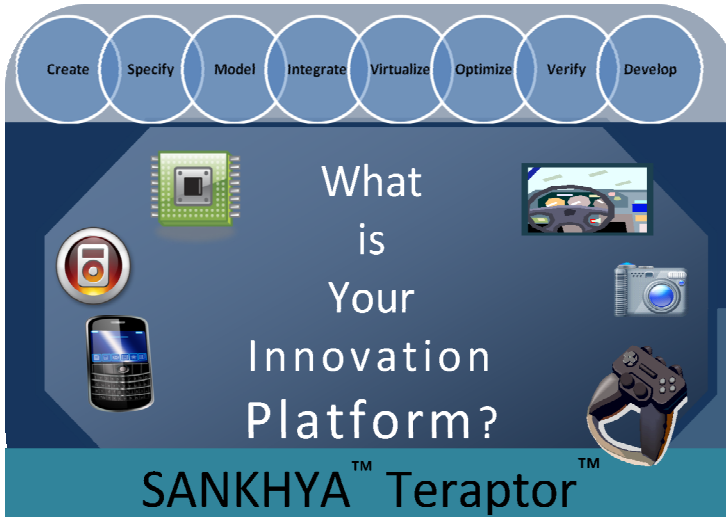


Technology developments and increasing globalization over the last 2 decades have fundamentally changed the way intelligent embedded systems are built today. New products often become obsolete in months. There is an increasing need for performing various activities of research, innovation and development concurrently and automate activities wherever possible. System Level Design or some times referred to as Electronic System Level Design is fast becoming the standard for building the next generation of embedded systems for markets ranging from consumer electronics, automotive electronics to defence electronics. Focus is shifting from the use of functional design automation tools to cross functional design automation platforms



Teraptor Designer brings architecture specification and modeling to programmable system design. Teraptor Designer includes languages for system modeling and a comprehensive set of meta-model driven tools for software development, model export, high level synthesis, test case generation and verification.

Teraptor Player creates fully functional virtual prototypes from Teraptor SMDL and SSDL Models.

Teraptor Channels (Planned) offer fully functional system models for consumer electronics, networking and automotive electronics.

Teraptor is a platform approach to create, specify, model, integrate, optimize, virtualize and develop the next generation of high performance intelligent devices. Teraptor is the perfect solution for small teams working on a new product to an entire organization engaged in developing multiple new products.

Attribute	Benefit to Designer	Translating to Better Design	Competitive Advantage
System Level Design	Use Architectural Languages for Design	Globally Optimized Solution	Low Cost, High Performance
Architectural Approach	Multi-Dimensional Design Automation	Comprehensive Solution	High Quality Products, Reduced Support Costs
Globally Optimized	Integrated Processor and System Design, Successful First Design	Reduced Gate Counts Direct Connection Architectures	Reduced Power Usage, Enhanced Yields (Lower Manufacturing Costs)
Higher Abstraction Designs	Greater Design Reuse and Automation	Improved Quality	Reduced Time to Market, Quick to develop Product Variants for Global Markets

Customer Success, Awards and Recognition

- ❖ Teraptor selected for digital set top box design by a US design team. The project included the creation of a new processor architecture for high performance, low power digital video processing, complete development of compiler tool chain and the creation of a functional virtual prototype
- ❖ Teraptor received Lockheed Martin India Innovation award during the year 2008
- ❖ Teraptor received support from Technology Development Board, Government of India - 2009
- ❖ Teraptor named global innovation from India by **Economic Times**.
- ❖ Teraptor selected for finishing school program by **JNTU 2009**
- ❖ Teraptor listed under multiple categories by **Garysmitheda.com** on their ESL Charts for 2009

For more information or to request a proposal contact sales@sankhya.com or visit <http://www.sankhya.com/contact.html>

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