The SANKHYA Dynamically Targetable Tools Framework™ (DTTF) and SANKHYA CPU Design Kit (CDK) provide a set of dynamically targetable compiler tools, which can be quickly configured for new processors or processor variants. These tools obtain information about the processor from SANKHYA Machine Description Language (SMDL®) model of the processor.

SMDL is a simple yet powerful language for describing a processor. Using SMDL, processor information required by tools like code generator, assembler and simulator can be specified. SMDL allows the specification of a processor as a hierarchy of instruction bundles, instructions and operands.

The SMDL Model Space Explorer is a CDK Tool powered by DTTF that allows the verification of processor implementation using it’s SMDL model for a given model space.

**Key Features**

- Automatically generates test cases containing variable length instruction sequence.
- Generates start state and end state (for reference).
- Automatically executes each test using a user defined script and compares the results and generates reports indicating number of tests run and number of failures.
- Auto stop feature to stop on failure.
- Rerun feature to rerun a specific test case from a previous test log.

**Benefits**

- Little or no extra effort required for generating tests.
- Ability to restrict the test space to a subset of its instruction set for faster testing.
- Simple SMDL Model Space extensions for controlling instruction and instruction sequence generation, controlling the subset of processor state to be computed and verified for each test.
- User defined scripts allow the use of the tool with any execution environment like ISA simulator, HDL simulator, FPGA or real target.

**Host Platforms Supported**

- Windows 2000/XP

**Availability:** Now

**Pricing Model:** Node Locked Annual Subscription Licensing per developer/year with Standard Support and Premium Support options. For pricing, please email sales@sankhya.com

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